

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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First Named Inventor : Fu Chiung CHONG  
Serial No. : 09/979,551  
Patent No. : 6,812,718  
Issue Date : 11-02-2004  
Confirmation Number : 4999  
Examiner : Jermele M. Hollington  
Title : MASSIVELY PARALLEL INTERFACE FOR  
ELECTRONIC CIRCUITS  
Attorney Docket No. : NNEX0002

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August 13, 2010

Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**NOTIFICATION OF LOSS OF ENTITLEMENT TO SMALL ENTITY STATUS**

Pursuant to 37 CFR § 1.27(g)(2), you are hereby notified that small entity status in connection with the above-referenced patent is no longer appropriate. Therefore, please change the status for **NanoNexus, Inc.** to **LARGE entity**.

Applicant believes no fees are due however, if fees are deemed necessary, the Commissioner is hereby authorized to charge any fees and credit any overpayments to Deposit Account 07-1445 (Order No. NNEX0002).

Respectfully Submitted,



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